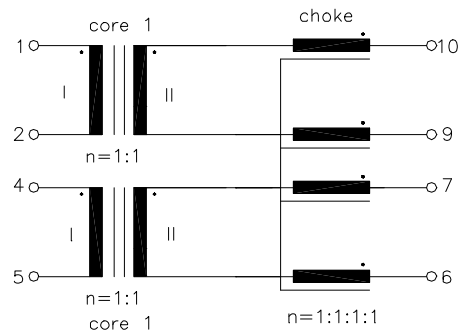


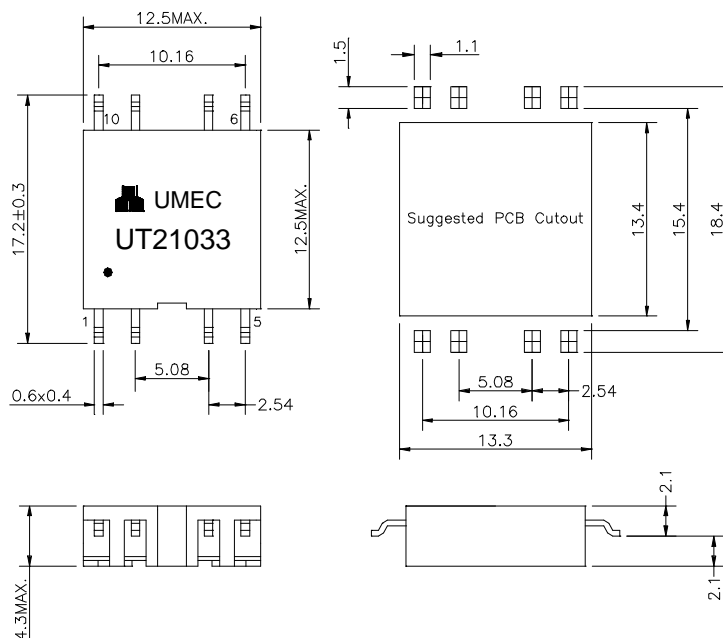
| | | | |
|--------------|---|------|-------|
| UM PART NO.: | SPECIFICATION | REV. | |
| UT21033 | S-Interface Module(with choke:100uH) For PCMCIA | A0 | 01/05 |

Characteristic data:

$RI \leq 1.8\Omega$
 $R_{II} \leq 2.4\Omega$ (include choke)
 $T_u(\text{amb}) \leq 60^\circ\text{C}$

Schematic diagram:

Electrical Specification at 25°C:

- 1.) $L_I \geq 22\text{mH}$, at 10KHz 100mV (core 1,2)
- 2.) Polarity and turns ratio tolerance: $\pm 1\%$ (core 1,2)
- 3.) $C_{w/w} \leq 80\text{pF}$, (NI to NII), at 10KHz 100mV (core 1,2)
- 4.) $C_d I \leq 25\text{pF}$, at 1MHz 1V (core 1,2)
- 5.) $L_s I \leq 1.5\mu\text{H}$, (NII shorted), at 100KHz 100mV (core 1,2)
- 6.) HI-pot test: $U_p = 1.5\text{KVrms}$, 2s (NI to NII) (core 1,2,choke)

Dimension:


Note: Specifications are subject to change without prior notice.

UNIT: mm

Tolerances: $\pm 0.25\text{mm}$

E10-013-B