



ISDN S₀- INTERFACE DUAL TRANSFORMER/MODULES

MICRO-Package (12,9x7,2x6 mm)

- The transmission pulse mask and impedance according to **ITU-T-I.430**
- The series products are suitable for high temperature 220±5°C IR reflow
- Smallest dual package for saving space of over half volume
- Dimension: L/W/H(inch)0.50"x0.375"x0.220"(12.9x9.7x6mm)
- Operating Temperature Range: -40°C to +85°C
- Storage Temperature Range: -40°C to 105°C
- 1500Vrms Isolation Voltage

IC- SELECTION GUIDE:

Chip Manufacturer	Chip Designation	Dual Transformer	Module With 50uH CMC	Module With 100uH CMC	Module With 500uH CMC
AMD	AM79C30A/32A	UT21637-S	UT21637-05S	UT21637-10S	UT21637-50S
Cologne Chip	HFC-S/S+/SP(A) SPCI(A)	UT21637-S	UT21637-05S	UT21637-10S	UT21637-50S
Lucent(AT&T)	T7234/T7250/ T7256/T7259	UT21639-S	UT21639-05S	UT21639-10S	UT21639-50S
	T7901/T7903	UT21637-S	UT21637-05S	UT21637-10S	UT21637-50S
Mietec	MTC-2072	UT21637-S	UT21637-05S	UT21637-10S	UT21637-50S
Mitel	MT8930/8931	UT21637-S	UT21637-05S	UT21637-10S	UT21637-50S
Motorola	MC145474/145475	UT21638-S	UT21638-05S	UT21638-10S	UT21638-50S
	MC145574	UT21639-S	UT21639-05S	UT21639-10S	UT21639-50S
National	TP3420/3421	UT21637-S	UT21637-05S	UT21637-10S	UT21637-50S
SGS Thomson	ST5420/5421	UT21637-S	UT21637-05S	UT21637-10S	UT21637-50S
Infineon	PEB2080/2081 2084/2085/2086 PSB2186	UT21637-S	UT21637-05S	UT21637-10S	UT21637-50S
	PSB21381/21382 PSB21383/21384 PSB21150;PEB3086/ -PEB3081; PSB2154;PEB20590 PEB20591	UT21638-S	UT21638-05S	UT21638-10S	UT21638-50S
Yamaha	YM7405B	UT21637-S	UT21637-05S	UT21637-10S	UT21637-50S

1.CMC : Common Mode Choke.

2. Various of choke inductance are available.

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ELECTRICAL SPECIFICATION @25°C:

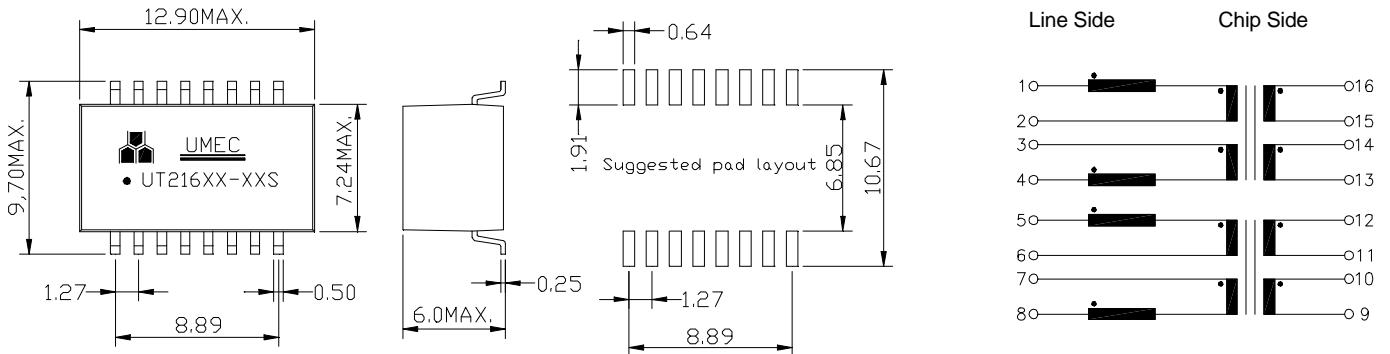
PART NO.	Turns Ratio (Line:Chip)(±2%)	OCL Line Side (mH Min.)	L _L Line side (uH Max)	C _{w/w} (pF Max)	DCR Line Side (Ω Nom.)	DCR Chip Side (Ω Nom.)
UT21637-S	1CS:2CS	25	3	80	1.3	3.8
UT21638-S	1CS:1CS	25	3	80	1.3	1.9
UT21639-S	1CS:2.5CS	25	3	110	1.4	4.8

PART NO.	Common mode choke							
	-S		-05S		-10S		-50S	
	OCL (uH)	DCR (Ω max.)	OCL (uH)	DCR (Ω max.)	OCL (uH)	DCR (Ω max.)	OCL (uH)	DCR (Ω max.)
UT21637-**S	None		50	0.3	100	0.5	500	0.7
UT21638-**S	None		50	0.3	100	0.5	500	0.7
UT21639-**S	None		50	0.3	100	0.5	500	0.7

Test Condition:

1. Inductance (Main Transformers) & C_{w/w}: 10KHz 100mVrms.
2. Inductance of CMC: 100KHz 20mVrms
3. Leakage Inductance: 100KHz 100mVrms.

PACKAGES & SCHEMATICS



Unit : mm

Tolerance:±0.25mm

*Specifications are subject to change without prior notice.

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